

Fig 2

005220-00000000

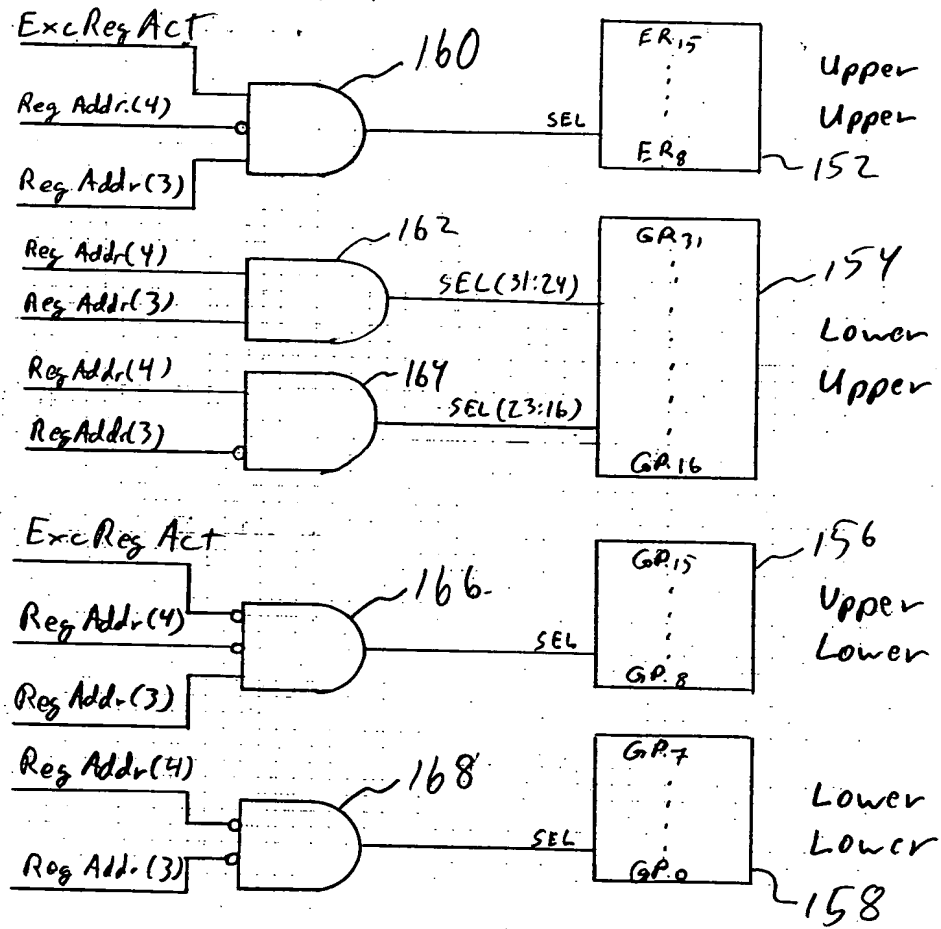


Fig 3

```

graph TD
    200[Set FP Control Status Register  
EnHi <- VPN2, ASID  
Context <- VPN2  
Set Cause Register  
EXCCode, CE] --> 202{BEV}
    202 -- "= 0 (normal)" --> 206[Base <- 0xFFFF FFFF 8000 0000  
(unmapped, cached)]
    202 -- "= 1 (bootstrap)" --> 208[Base <- 0xFFFF FFFF BFC0 0200  
(unmapped, uncached)]
    206 --> 210{Fast Interrupt?}
    208 --> 210
    210 -- "[Enabled, (Bit 0, register 1/16) asserted]" --> 212[offset = 0x8e0]
    210 -- "No" --> 214{Cause.IV}
    214 -- "= 0" --> 216[offset = 0x180]
    214 -- "= 1" --> 218[offset = 0x200]
    212 --> 220[PC <- Base + offset]
    216 --> 220
    218 --> 220
    
```

Fig 4

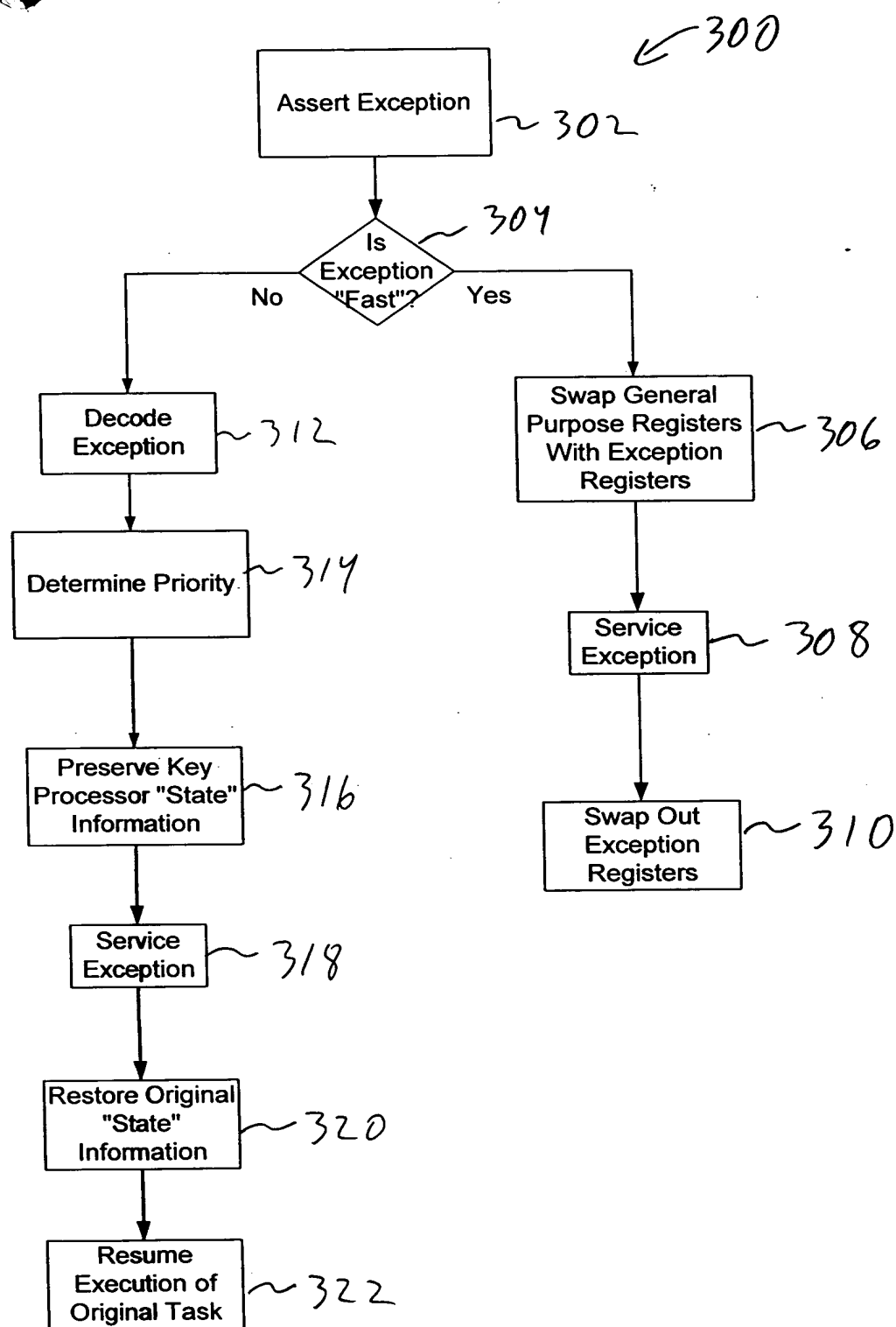
[illegible]

Fig 5